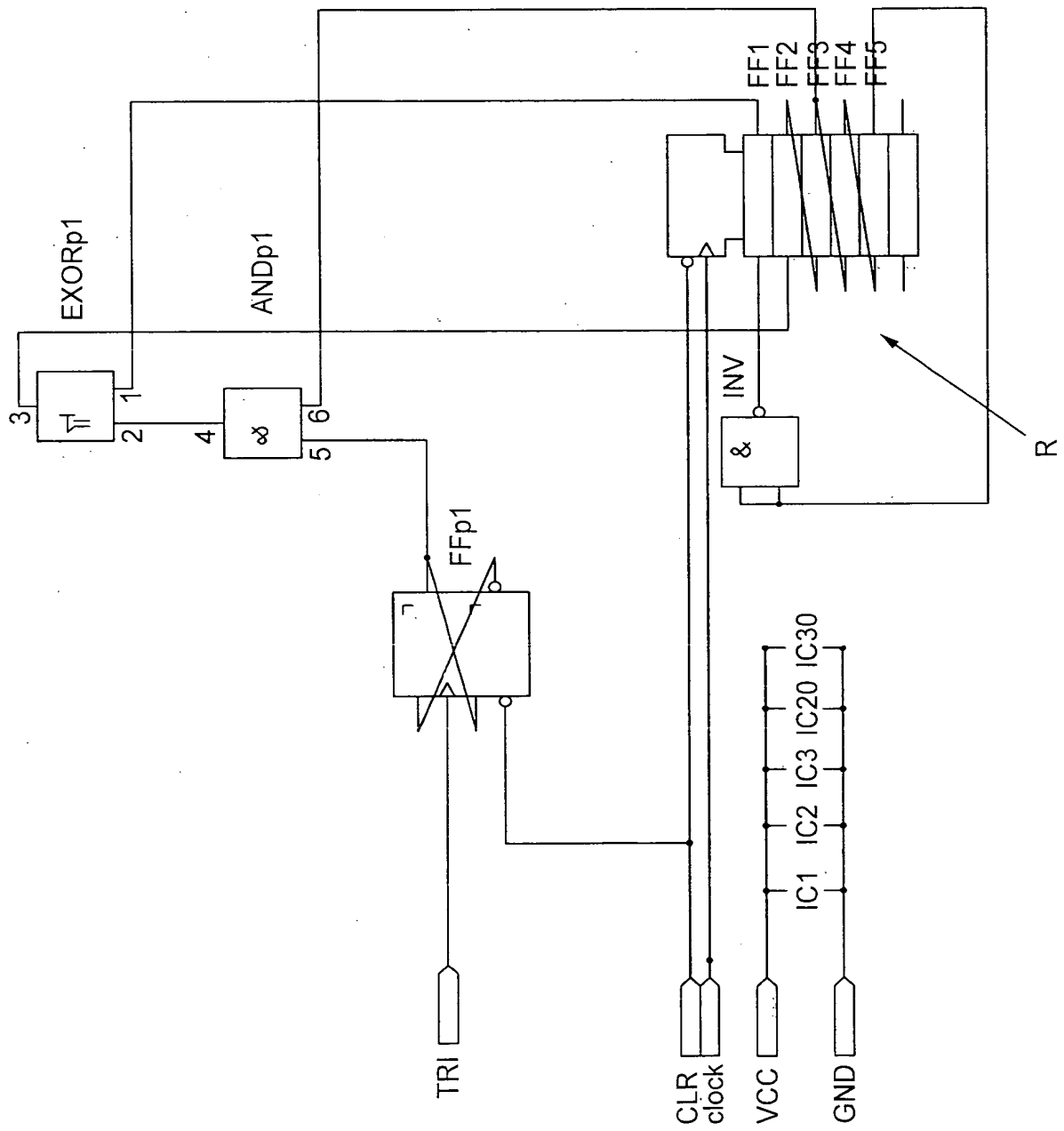


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Fig. 1



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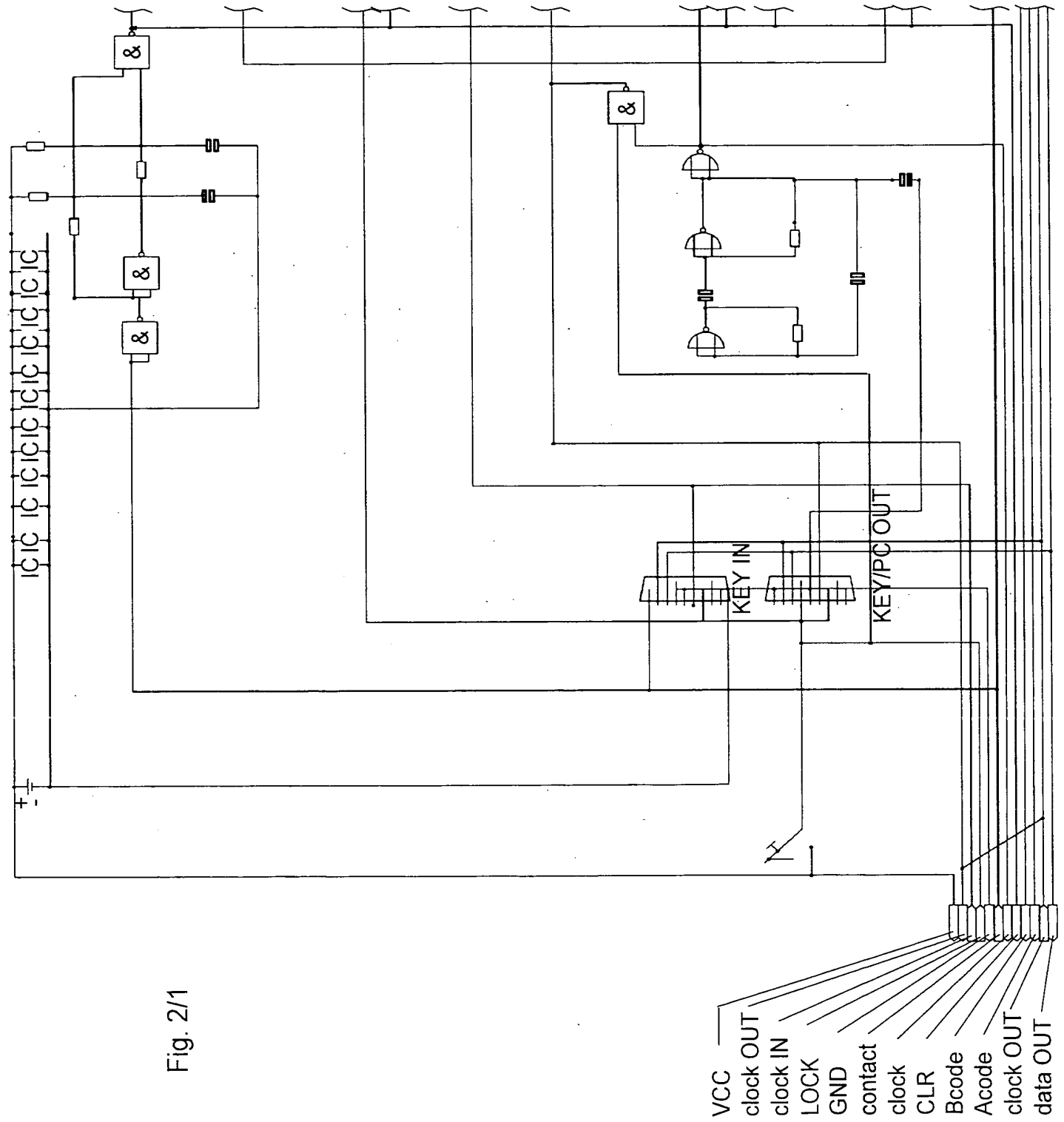
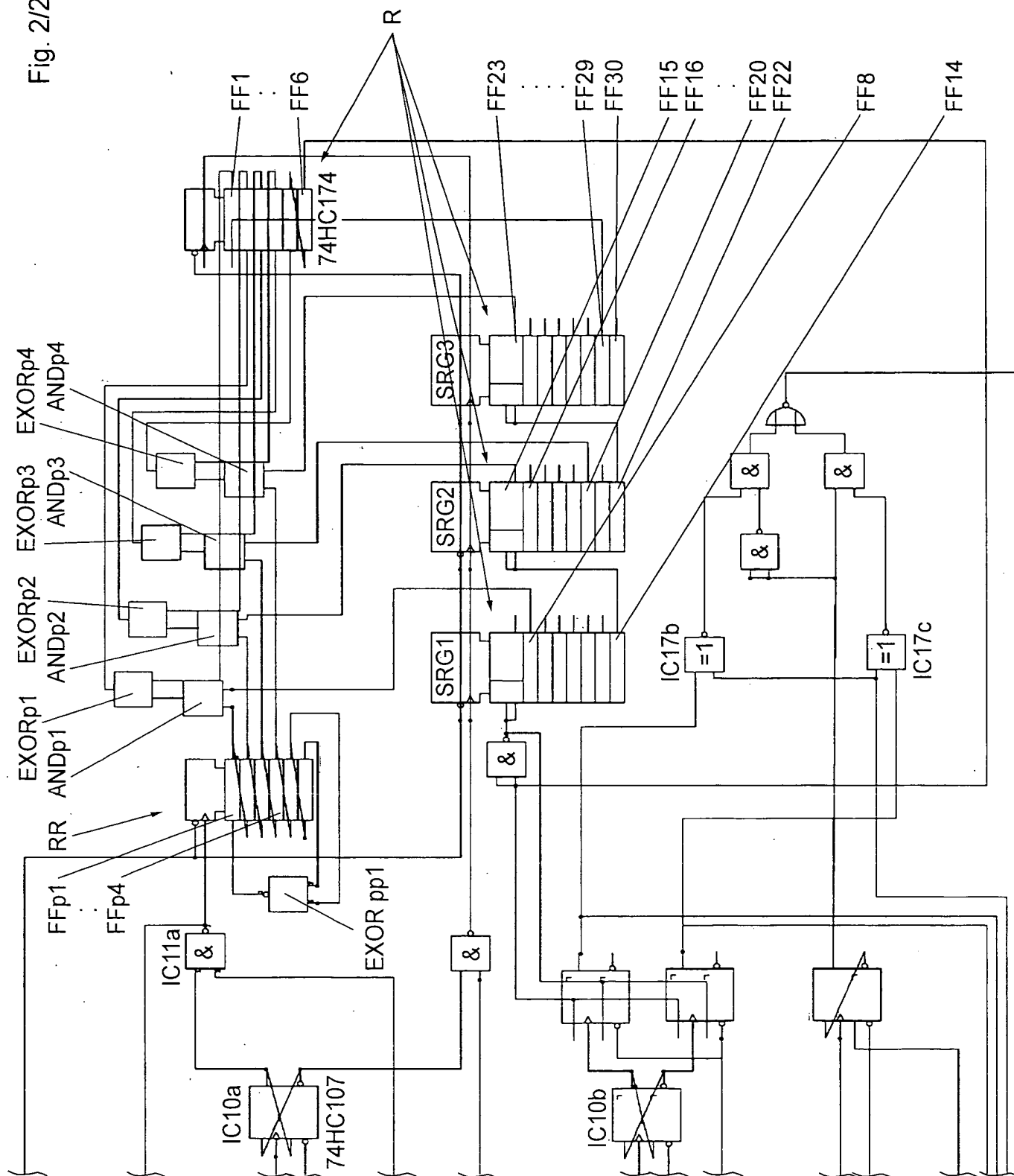


Fig. 2/1

VCC
clock OUT
clock IN
LOCK
GND
contact
clock
CLR
Bcode
Acode
clock OUT
data OUT

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Fig. 2/2



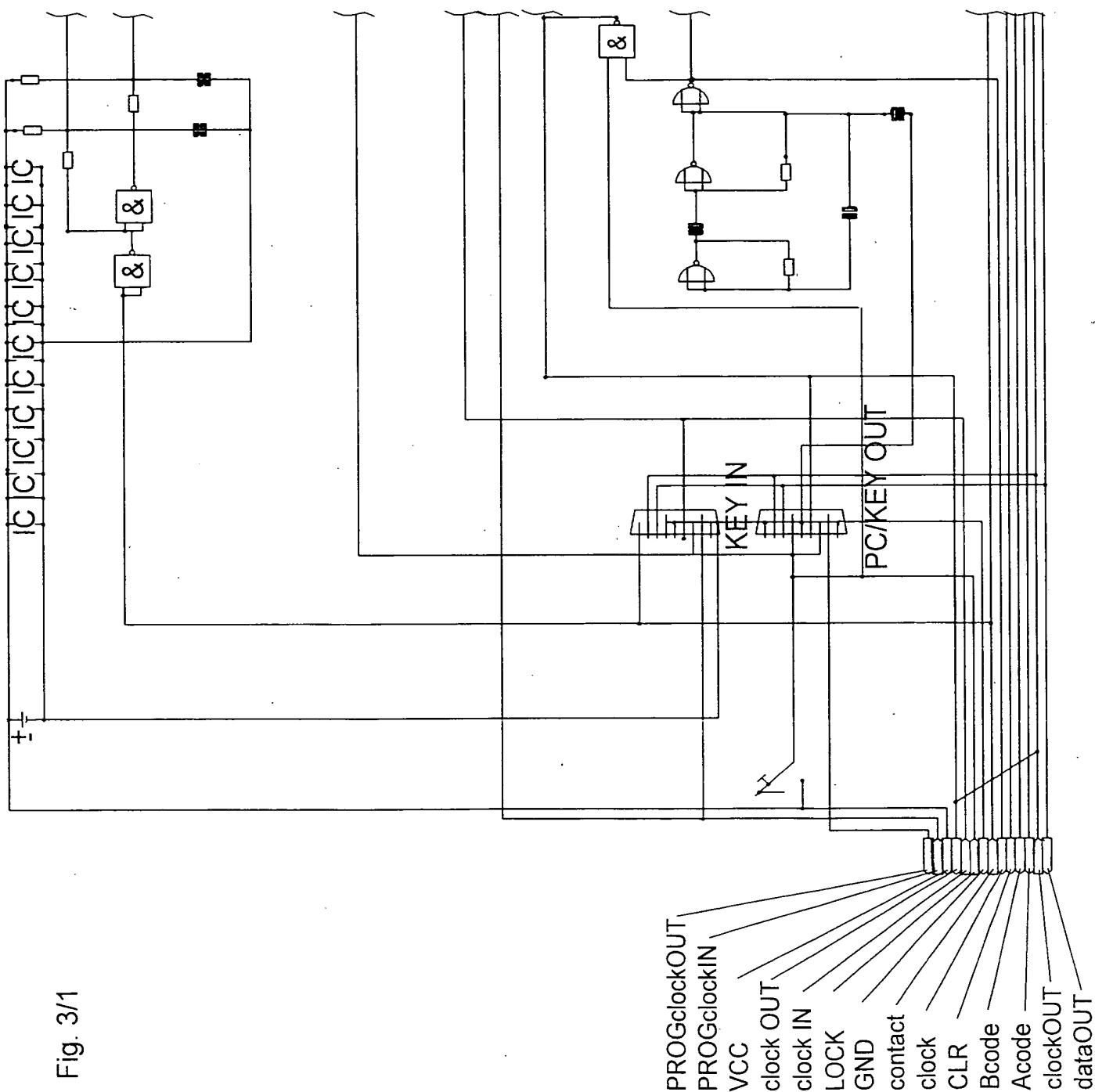


Fig. 3/1

Fig. 3/2

